

a plurality of data lines insulated from and intersecting said gate lines, said data lines and intersecting gate lines defining a plurality of cells, at least one cell including,

a pixel electrode,

a thin film transistor connected to one of the data lines and one of the gate lines defining the cell,

a storage capacitor, and

a metallic pattern including a drain electrode of the thin film transistor and a storage electrode of the storage capacitor in a single layer, and being electrically connected to the pixel electrode.

21. (Three Times Amended) A liquid crystal device having a thin film transistor, comprising:


a plurality of gate lines formed on a substrate;


a plurality of data lines insulated from and intersecting said gate lines, said data lines and intersecting gate lines defining a plurality of cells, at least one cell including,

a pixel electrode,

a thin film transistor interposed between one of the data lines and the pixel electrode and including a source electrode connected to the one of the data lines, a gate electrode connected to one of the gate lines, a drain electrode,

and

 a storage capacitor including a storage electrode and a drain electrode in a single layer, the storage capacitor being connected to the pixel electrode.

 23. (Twice amended) A method of manufacturing a thin film transistor substrate, comprising:

forming a gate line having a gate electrode on a transparent substrate;
forming a gate insulating layer on the gate electrode;
forming a semiconductor layer on the gate insulating layer;
forming a data line having a source electrode, and a metallic pattern including a drain electrode part and a storage electrode part;
forming a semiconductor layer over at least a portion of one of the gate electrodes, at least a portion of one of the source electrode, and at least a portion of the drain electrode part in a single layer;
forming a protective film over the entire surface; and
forming a pixel electrode over the protective film.
